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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	10/538,563
Filing Date	June 15, 2005
First Named Inventor	Edwin Rijpkema
Art Unit	2473
Examiner Name	Kao, Jutai
Attorney Docket Number	EIN-NL021330 (STNX01-21330)

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ *Number ⁴ *Kind Code ⁵ (if known)				
/J.K./		JP 2000-151703	05/30/2000	NEC Corp.		
/J.K./		JP 2000-295228	10/20/2000	NEC Corp.		

Examiner Signature	/Jutai Kao/	Date Considered	10/04/2010
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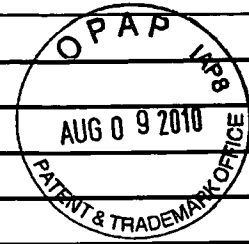
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Sheet	2	of	2



NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/J.K./		Edwin Rijpkema, et al., "A Router Architecture for Networks on Silicon", Proceedings of Progress 2001, 2nd Workshop on Embedded Systems, pg. 1-8.	
/J.K./		Mamoru Takajyo, et al., "Improvement for Packet Priority of the Packet Switch with Multiple Input Queues for Every Output Port", The Institute of Electronics, Information and Communication Engineers, Technical Report of IEICE SSE97-13, April 1997, 8 pages.	
/J.K./		Translation of Office Action dated May 22, 2009 in connection with Japanese Patent Application No. 2004-561738.	

Examiner Signature	/Jutai Kao/	Date Considered	10/04/2010
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